

ISSCC SPECIAL TOPIC SESSIONS

- ISSCC'02

SPECIAL TOPIC SESSIONS - 2002

SE1: Inductance: Implications and Solutions for High-Speed Digital Circuits

Co-organizer/Co-chair: William J. Bowhill, Intel Corp., Shrewsbury, MA
Co-organizer: Anantha Chandrakasan, MIT, Cambridge, MA
Co-chair: Stephen Kosonocky, IBM, Yorktown Heights, NY

Topics: Inductance Extraction and Modeling

David Blaauw, Univ. of Michigan
Kaushik Gala, Motorola Inc., Austin, TX

On-Chip Signaling

Shannon Morton, Silicon Graphics, Boston, MA

Clock Distribution

Phillip Restle, IBM, Yorktown Heights, NY
Xuejue Huang, Univ. of California-Berkeley, CA

The Chip Interface

Claude Gauthier, Sun Microsystems, Sunnyvale, CA
Brian Amick, Sun Microsystems, Austin, TX

SE2: Low-Voltage Design for Portable Systems

Organizer/Co-chair: Wanda Gass, Texas Instruments, Dallas, TX
Co-chair: Thucydides Xanthopoulos, Cavium Networks, Marlboro, MA

Topics: Technology, Architecture and Applications

Robert Brodersen, Prof., Univ. of California-Berkeley, CA

When MOSFET Switches Become MISFET Dimmers

Dennis Buss, Vice President, Texas Instruments, Dallas, TX

RF Circuits at Low Voltage

Asad Abidi, Prof., Univ. of California-Los Angeles, CA

Leakage Reduction in Digital CMOS Circuits

Shekhar Borkar, Intel Fellow and Director, Intel Corp., Hillsboro, OR

Robustness of Digital Circuits @ Low Voltage

Harry Veendrick, Research Fellow, Philips, Eindhoven, The Netherlands